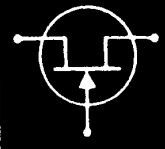


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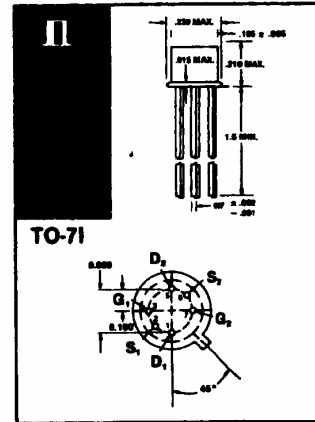
	ULTRA LOW NOISE DUAL MATCHED N-CHANNEL FIELD EFFECT TRANSISTOR	CD860
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GEOMETRY 424

**HIGH PERFORMANCE DIFFERENTIAL
 AMPLIFIERS**

- $1.4 \text{ nV/Hz}^{1/2} \text{ en @ 1 kHz}$
- Min. Operating G_m 25,000 μmho
- Matched VPO and G_m

The CD860 is a high G_m/I_D low noise junction F.E.T. for low level amplifier use. The min. G_m of 25,000 assures a voltage gain of 25 min. with a 1K drain load. As a source follower it has a typical output impedance of 25 ohms. The 10mA operating point is easily held due to its low pinchoff voltage and is very close to it's zero T.C. point for temperature stable operation.



ELECTRICAL DATA ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	VALUE	UNITS
Drain to Source Voltage	BV_{DSO}	20	Volts
Drain to Gate Voltage	BV_{DGO}	20	Volts
Gate to Source Voltage	BV_{GSO}	-20	Volts
D.C. Forward Gate Current	I_{GF}	50	mA
Junction Temp. (Operating & Storage)	T_J	-65°C to +200°C	
Power Dissipation (Free Air)	P_D	400 mW	
Lead Temp. (@ 1/16" ± 1/32" from case)	T_L	240° for 10 sec.	
Derating Factor (Free Air)	D_F	2.3 mW/C°	

ELECTRICAL CHARACTERISTICS: $T_A = 25^\circ\text{C}$ (UNLESS OTHERWISE STATED)

PARAMETER	SYMBOL	CONDITION	Min.	Typ.	Max.	UNITS
Gate Leakage Current	I_{GSS}	$V_{GS} = -10V, V_{DS} = 0$	0.1	3.0		nA
Gate Leakage Current	I_{GSS}	$V_{GS} = -10V, V_{DS} = 0, T_A = 85^\circ\text{C}$		5	100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 10V, V_{GS} = 0$	10	100		mA
Pinch-Off Voltage	V_{PO}	$V_{GS} = 10V, I_D = 0.1\text{mA}$	0.3	1.5	3.0	Volts
Transconductance	g_m	$V_{GS} = 10V, I_D = 10\text{mA}, f = 1\text{kHz}$	25	40		mmho
Input Capacitance	C_{iss}	$V_{DS} = 10V, I_D = 10\text{mA}, f = 140\text{kHz}$		30	35	pf
Reverse Xfer Cap	C_{res}	$V_{GS} = 10V, f = 140\text{kHz}$		17	20	pf
Gate to Drain Capacitance	C_{gd}	$V_{GS} = -10V, f = 140\text{kHz}$		20		pf
Output Admittance	Y_{os}	$V_{GS} = 10V, I_D = 10\text{mA}$		50	100	μmho
Input Noise Voltage	e_n	$V_{DS} = 5V, I_D = 10\text{mA}, f = 1\text{kHz}$		1.4	2.0	$\text{nV/Hz}^{1/2}$
Input Noise Voltage	e_n	$V_{DS} = 5V, I_D = 10\text{mA}, f = 10\text{Hz}$		6.0	10	$\text{nV/Hz}^{1/2}$
Input Noise Voltage	$e_n \text{ TOTAL}$	$V_{DS} = 5V, I_D = 10\text{mA}, f = 10\text{Hz to } 20\text{kHz}$		0.4	0.6	μVrms
Equivalent Open Ckt. Input Noise Current	i_n	$R_{source} < 100K \Omega, f = 1\text{kHz}$.01		$\text{pA/Hz}^{1/2}$
VPO Match	V_{PO1}, V_{PO2}	$V_{GS} = 10V, I_D = 0.1\text{mA}$			25	mV
G_m Match	G_{m1}, G_{m2}	$V_{GS} = 10V, I_D = 10\text{mA}, f = 1\text{kHz}$			5	%