

2N2060

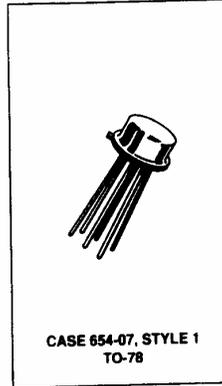
Dual NPN Silicon
Small-Signal Transistor

... designed for general-purpose amplifier applications.

CRYSTALONCS
2805 Veterans Highway
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Ronkonkoma, N.Y. 11779

Operation

MAXIMUM RATINGS				
Rating	Symbol	Value		Unit
Collector-Emitter Voltage	V _{CEO}	60		Vdc
Collector-Base Voltage	V _{CBO}	100		Vdc
Emitter-Base Voltage	V _{EBO}	7.0		Vdc
Collector Current — Continuous	I _C	500		mAdc
		One Die	All Die Equal Power	
Device Dissipation	P _T	540	600	mW
@ T _A = 25 °C		3.08	3.43	mW/°C
Derate above 25 °C		1.5	2.12	Watts
@ T _C = 25 °C		8.6	12.1	mW/°C
Derate above 25 °C				
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-65 to 200		°C



ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted.)				
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage ⁽¹⁾ (I _C = 30 mAdc, I _B = 0)	V _{(BR)CEO}	60	—	Vdc
Collector-Emitter Breakdown Voltage ⁽¹⁾ (I _C = 100 mAdc, R _{BE} = 10 ohms max)	V _{(BR)CER}	80	—	Vdc
Collector-Base Breakdown Voltage (I _C = 100 μAdc, I _E = 0)	V _{(BR)CBO}	100	—	Vdc
Emitter-Base Breakdown Voltage (I _E = 100 μAdc, I _C = 0)	V _{(BR)EBO}	7.0	—	Vdc
Collector Cutoff Current (V _{CB} = 80 Vdc)	I _{CBO}	—	2.0	nAdc
(V _{CB} = 80 Vdc, T _A = 150 °C)		—	10	μAdc
Base Cutoff Current (V _{BE} = 5.0 Vdc)	I _{EBO}	—	2.0	nAdc

(1) Pulsed. Pulse Width 250 to 350 μs. Duty Cycle 1.0 to 2.0%.

(continued)

2N2060JAN SERIES

ELECTRICAL CHARACTERISTICS — continued (T _A = 25 °C unless otherwise noted.)				
Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS				
DC Current Gain (I _C = 10 μAdc, V _{CE} = 5.0 Vdc) (I _C = 100 μAdc, V _{CE} = 5.0 Vdc) (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc) (I _C = 10 mAdc, V _{CE} = 5.0 Vdc)(1) (I _C = 100 μAdc, V _{CE} = 5.0 Vdc, T _A = -55°C)	h _{FE}	25 30 40 50 10	75 90 120 150 —	—
Collector-Emitter Saturation Voltage (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{CE(sat)}	—	0.3	Vdc
Base-Emitter Saturation Voltage (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{BE(sat)}	—	0.9	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 to 1.0 MHz)	C _{obo}	—	15	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 0.1 to 1.0 MHz)	C _{ibo}	—	85	pF
Noise Figure (I _C = 300 μAdc, V _{CE} = 10 Vdc) (f = 1.0 kHz, R _G = 510 ohms) (f = 10 kHz, R _G = 1.0 kohms)	NF	—	8.0 8.0	dB
Current Gain (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc, f = 1.0 kHz)	h _{fe}	50	150	—
Small-Signal Current Transfer Ratio, Magnitude (I _C = 50 mAdc, V _{CE} = 10 Vdc, f = 20 MHz)	h _{fe}	3.0	25	—
Input Impedance (I _C = 1.0 mAdc, V _{CB} = 5.0 Vdc, f = 1.0 kHz)	h _{ib}	20	30	ohms
Input Impedance (I _C = 1.0 mAdc, V _{CB} = 5.0 Vdc, f = 1.0 kHz)	h _{ie}	1.0	4.0	kohms
Output Admittance (I _C = 1.0 mAdc, V _{CB} = 5.0 Vdc, f = 1.0 kHz)	h _{oe}	—	16	μmhos
MATCHING CHARACTERISTICS				
DC Current Gain Ratio (I _C = 100 μAdc, V _{CE} = 5.0 Vdc)(2) (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc)(2)	h _{FE1} /h _{FE2}	0.9 0.9	1.0 1.0	—
Base-Emitter Voltage Differential (I _C = 100 μAdc, V _{CE} = 5.0 Vdc) (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc)	V _{BE1} - V _{BE2}	— —	5.0 5.0	mVdc
ΔBase-Emitter Voltage Differential over Temperature (I _C = 100 μAdc, V _{CE} = 5.0 Vdc, T _A = 25 to -55 °C) (I _C = 100 μAdc, V _{CE} = 5.0 Vdc, T _A = 25 to 125 °C)	Δ(V _{BE1} - V _{BE2})	— —	0.8 1.0	mVdc
Collector to Collector Leakage Current (V _{C1C2} = 100 Vdc)	I _{C1C2}	—	100	nAdc

(1) Pulsed. Pulse Width 250 to 350 μs. Duty Cycle 1% to 2.0%.
 (2) The larger number will be placed in the denominator.